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Category:Virus writers Category:Malware1. Field of the Invention The present invention relates to a bit line contact structure in a semiconductor device and a method for fabricating the same, and more particularly, to a bit line contact structure for a semiconductor device that improves the reliability of an interconnection structure. 2. Description of the Related Art As integration of semiconductor devices increases, a design rule of devices is being reduced. Also, a contact structure for connecting an upper conductive layer to a semiconductor substrate has been developed to be a self-aligned contact (SAC) structure. In the SAC structure, an upper surface of a bit line is self-aligned with a lower surface of a gate electrode and a lower surface of a source/drain region. Therefore, the SAC structure reduces a contact area between an interconnection layer and a semiconductor substrate, thereby reducing a contact resistance. A high aspect ratio is required for the contact hole which the SAC structure, and it is difficult to perform the SAC process due to a step coverage characteristic. To overcome the above-described problems, a buried contact (BC) structure has been developed as a new contact structure. The BC structure is a contact structure in which an interconnection layer is formed on a lower conductive layer and a contact hole is formed in an insulation layer disposed on an upper portion of the interconnection layer. In the BC structure, a lower conductive layer is formed on a semiconductor substrate, and an insulation layer is deposited on the semiconductor substrate and the lower conductive layer. Then, a photoresist layer is deposited on the insulation layer and patterned to form a contact hole exposing the lower conductive layer. The insulation layer is etched using the contact hole as an etch mask. Then, the lower conductive layer is etched using the contact hole as an etch mask. In the above process, a conductive layer in a contact region is generally formed of silicon (Si) in order to decrease a contact resistance of a source/drain region. However, the lower conductive layer formed of Si is not etched sufficiently when the lower conductive layer is etched using the contact hole as an etch mask, so that a problem arises in that a margin between the contact region and the upper conductive layer is reduced. More than half of US consumers still hold a positive view of cryptocurrency, with 28% believing they have already purchased cryptocurrency in the past, the cryptocurrency advocacy 2d92ce491b